

## Understanding the Chip Market – Definition, Classification, Value Chain and Global Status

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- A chip (also known as an IC) is a sophisticatedly fabricated, silicon wafer that contains millions to billions of tightly integrated electronic components.
- The value of the global semiconductor industry has grown explosively from the Covid-19 period, reaching USD 700 mn in 2025, expected to reach USD 1,000 mn in 2030, implying the expansion of the chip market size.
- The quartet of the United States - China - Taiwan - South Korea are the names that control the global chip industry game, dividing the main roles in the global semiconductor industry value chain as shown below, implying that any geopolitical fluctuations related to this quartet will significantly affect the outlook for global chip production and prices. In particular, the US controls core technologies in the chip industry such as IP & EDA, Design, Equipment, China-Taiwan-Korea controls chip industry inputs such as semiconductors, silicon wafers and test and assembly (ATP) outputs.

### Definition of Chip

A chip (also known as an IC) is a sophisticatedly fabricated, silicon wafer that contains millions to billions of tightly integrated electronic components. Chips are likened to the "brain" of most modern electronic devices when they can perform billions of calculations per second related to memory storage (RAM) or processing (CPU/GPU/SoC) in terms of image display, camera processing, 5G/Wi-Fi connection, facial recognition... The process of making a semiconductor chip is one of the most complex technologies, usually taking about 3 months (12-16 weeks) with 700 steps per rigorous technical step on the silicon wafer (according to the article "Three months, 700 steps: Why it takes so long to produce a computer chip" published in the Washington Post).

### Sketch of the structure of a modern chip

	Parts	Function
Chip Case	Package (protective case, usually black plastic or ceramic, with metal feet)	Protects the chip core (die) from dust, moisture, and collisions, and helps connect to the motherboard (PCB), which partially dissipates heat.
	Bonding wires or bumps (gold or ball welded extension wires)	Connect the power from the chip (die) to the package pin to transmit data.
	Heat spreader	Quickly dissipates heat outwards so that chips don't overheat when operating under overload.
Chip (Die)	Substrate (thin, pure silicone base) (*)	As the mechanical and electrical foundation for the whole chip. Substrate provides a clean surface for building transistor and circuit layers, and conducts heat well.
	Well/Active layer (P-well, N-well doping zone)	Create a P or N type semiconductor region to form a transistor. This layer helps determine the location and type of transistor (NMOS/PMOS), helping the transistor to function properly (current on/off).
	Transistor layer (transistor layer, with Source, Drain, Channel, Gate)	Contains billions of microscopic transistors (transistors). Each transistor is a basic switch that turns on/off super-fast current to handle basic logic (0/1) of computing, temporary storage, and making up the CPU/GPU/NPU.
	Interconnect layers (10–20 layers of metal, usually Cu copper, low-k insulation interlayers)	As "highway systems" are intended to transmit signals and power between circuit sections without interference or slowdown. These include: <ul style="list-style-type: none"> <li>• Metal 0/M0 (lowest layer): Local connections between transistors that are close to each other.</li> <li>• Metal 1 to Metal 15+ (higher grade): Longer, thicker lines for long-distance signal transmission.</li> <li>• Via (through-hole): Longitudinal connection between metal layers.</li> </ul>
	Top metal layers	Used for power grid, clock distribution, and main signals. This layer provides stable power to the entire chip, synchronizes the timing of all transistors, and transmits big/fast signals.

The silicon wafer mentioned earlier is a large, round, thin silicon disc (usually 200mm, 300mm, or 450mm in diameter), made from high-grade pure silicon. Once finished, the wafer is cut into several small pieces, each called a die. The die is then thinned down to about 0.1–0.7 mm, becoming a silicon substrate – the core that contains the chip’s true circuitry (as shown in the construction above).

During the chip manufacturing process, lithography (or lithography machine, often referred to as ASML’s EUV/DUV) is the most important device, as shown by the use of ultraviolet light (UV or EUV) to “print” the circuit design from the mask to the photoresist coating on the wafer → deposition (deposition/doping to create a transistor, well/active layer, interconnect layer). This process is repeated 10–20 times to build each layer on top of each other (from the substrate → transistor layer → interconnect layers → top metal layers).

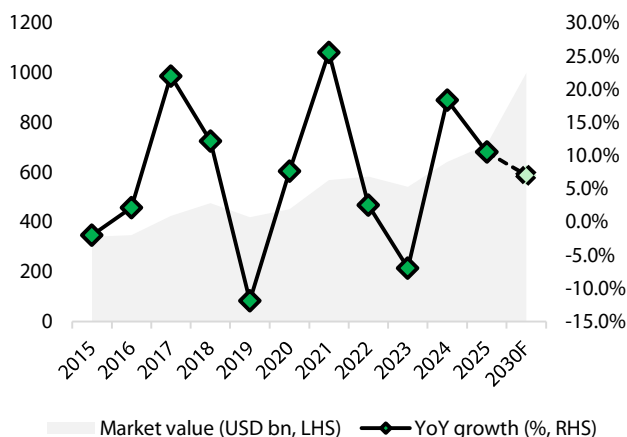
Source: Wikipedia, Intel, ResearchGate, RongViet Securities

**The story of AI emerging as an application leading the growth of the global chip industry in the medium term with a focus on the US - China**

The outlook for the chip manufacturing industry is closely tied to the semiconductor manufacturing industry as (1) 83.1% of global semiconductor production is applied to chip production (according to market.us), (2) die (the silicon core of the chip) is almost entirely composed of silicon atoms (extremely high purity).

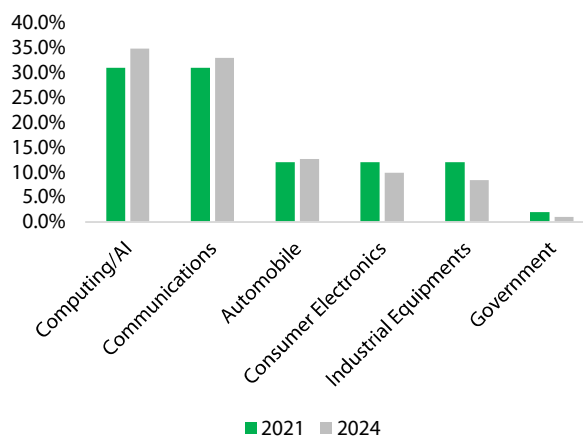
Currently, the demand for AI (data storage and processing in data centers) and the production of electronic devices (laptops, smartphones, tablets, etc.) are the biggest outputs for chips. Movements in these industries may affect the market value outlook of the semiconductor & chip industry.

**Market value of the global semiconductor industry has grown explosively from the Covid-19 period, reaching 700 mn in 2025, expected to reach USD 1,000 mn in 2030, implying the expansion of the size of the chip market**



Source: WSTS, RongViet Securities

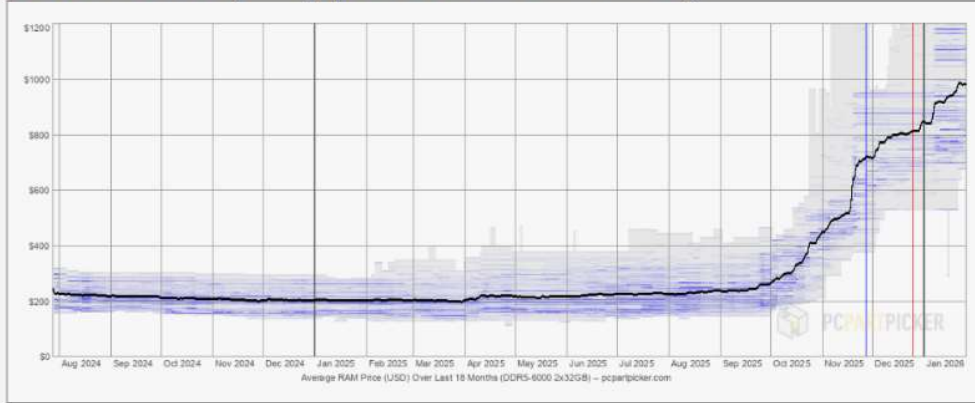
**The demand for AI data centers has emerged as a major driver of the growth of the semiconductor & chip industry (accounting for 33.0% of semiconductor production in use worldwide)**



Source: WSTS, RongViet Securities

**Huge demand from AI data centers and low inventories in chip manufacturers such as Samsung, SK Hynix, Micron, pushed the selling price of chips of all kinds (memory/processing chips) to increase sharply from Q3-2025, which is expected to continue for 2026.**

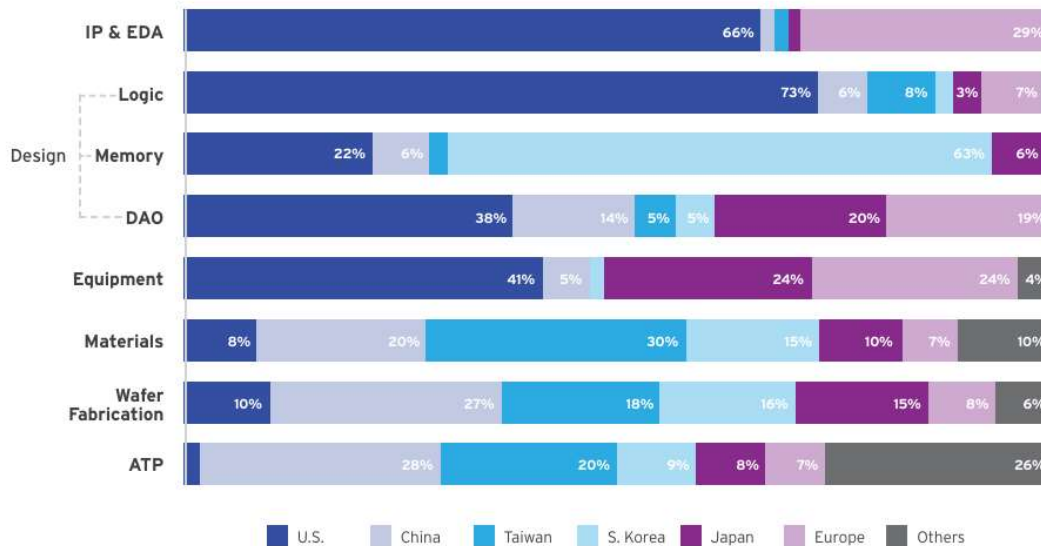
**DDR5-6000 2x32GB (Average price in USD over last 18 months)**



Source: pcpartpicker, RongViet Securities

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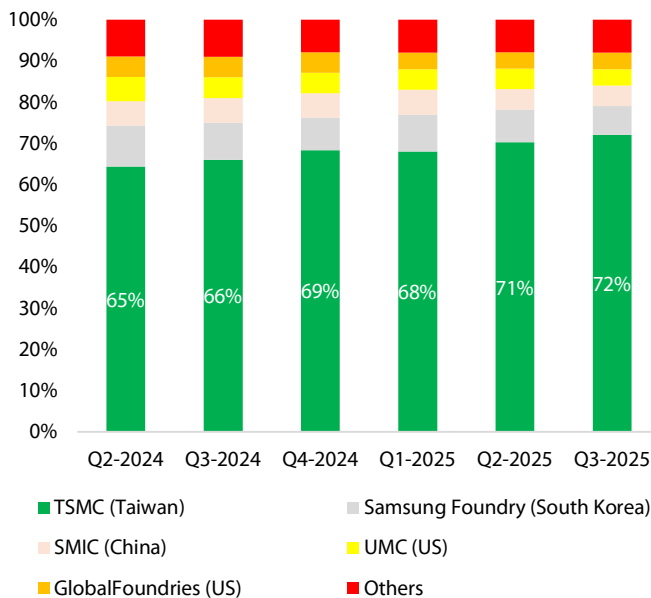
**Chip industry value-added by sub-sector and region in 2024 (%)**



Source: SIA, RongViet Securities. Annotate the stages in the table below

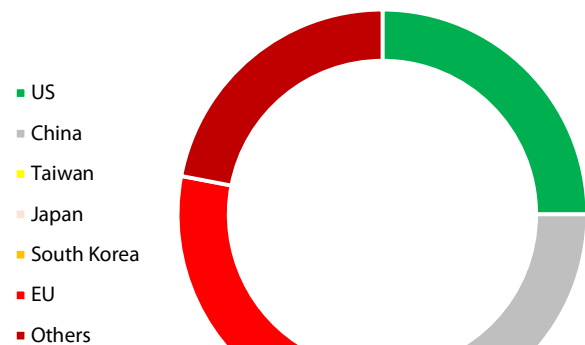
Stage	Details
IP & EDA (Intellectual Property & Electronic Design Automation):	IP is the available "core design blocks" (such as CPU cores, GPU cores, memory controllers) that the company can buy for use in its chips. EDA is a chip design software (tools such as Cadence, Synopsys for drawing circuit diagrams , simulations, and testing designs before production). This is the industry's first "brain" and creative tool.
Design	The stage of creating a complete chip design (blueprint). These include: <ul style="list-style-type: none"> <li>• Logic: Chips that handle complex logic (CPU, GPU, SoC for phones/AI).</li> <li>• Memory: Memory chip (DRAM, NAND flash).</li> <li>• DAO (Discrete/Analog/Other): Analog chips, discrete (sensors, optoelectronics, analog-digital signal conversion) or others that are simpler.</li> </ul>
Equipment	Machinery used to make chips on wafers (wafer fab equipment - WFE), such as lithography machines (ASML), etching, deposition... This is the most expensive and high-end technology to "print" circuits on silicon.
Materials	Required raw materials and chemicals: pure silicon, photoresist, special gases, cleaning chemicals...
Wafer Fabrication (Wafer Molding - front-end manufacturing)	The main process of chip production: silicon wafer removal, coating, circuit printing with ultraviolet light, engraving, doping... to create millions of transistors on silicon disks (foundries such as TSMC, Samsung, SMIC).
ATP (Assembly, Test, Packaging - back-end)	The last stage: cutting the wafer into individual chips, assembling, testing, packaging into complete chips to be attached to the board (such as in phones and computers).

**TSMC has a 72% share of the dedicated chip manufacturing market, supported by making the most of 4/5nm technology for AI GPUs and expanding CoWoS (Co-WoS) capabilities**



Source: Counterpoint, RongViet Securities

**The US & China share the top spot in terms of global chip consumption market share (%)**



Source: AIR University on the article *The Conflicting Economic and Security Imperatives of Semiconductor Supply-Chain Collaboration in the Indo-Pacific*, RongViet Securities

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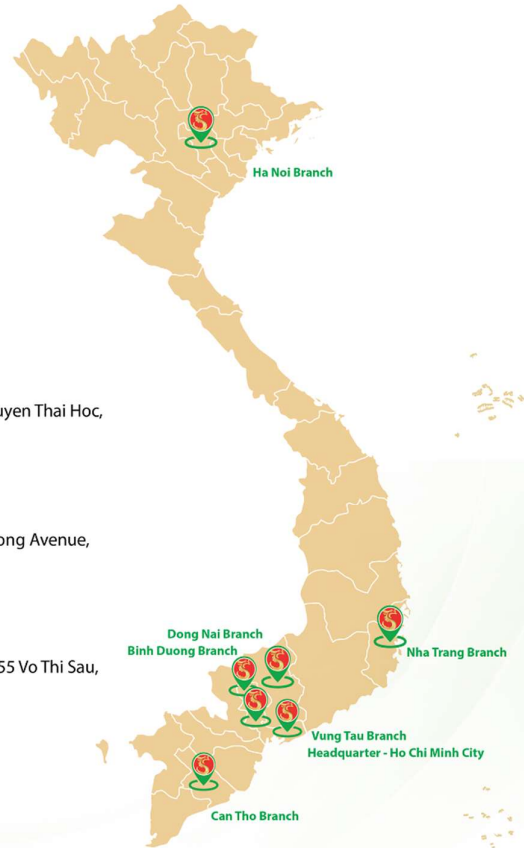
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